

Inventor(s): Thomas N. INDERMAUR

Contact Name: Alexander J. Neudeck (970) 898-4931

Attorney Docket No.: 10007795-1

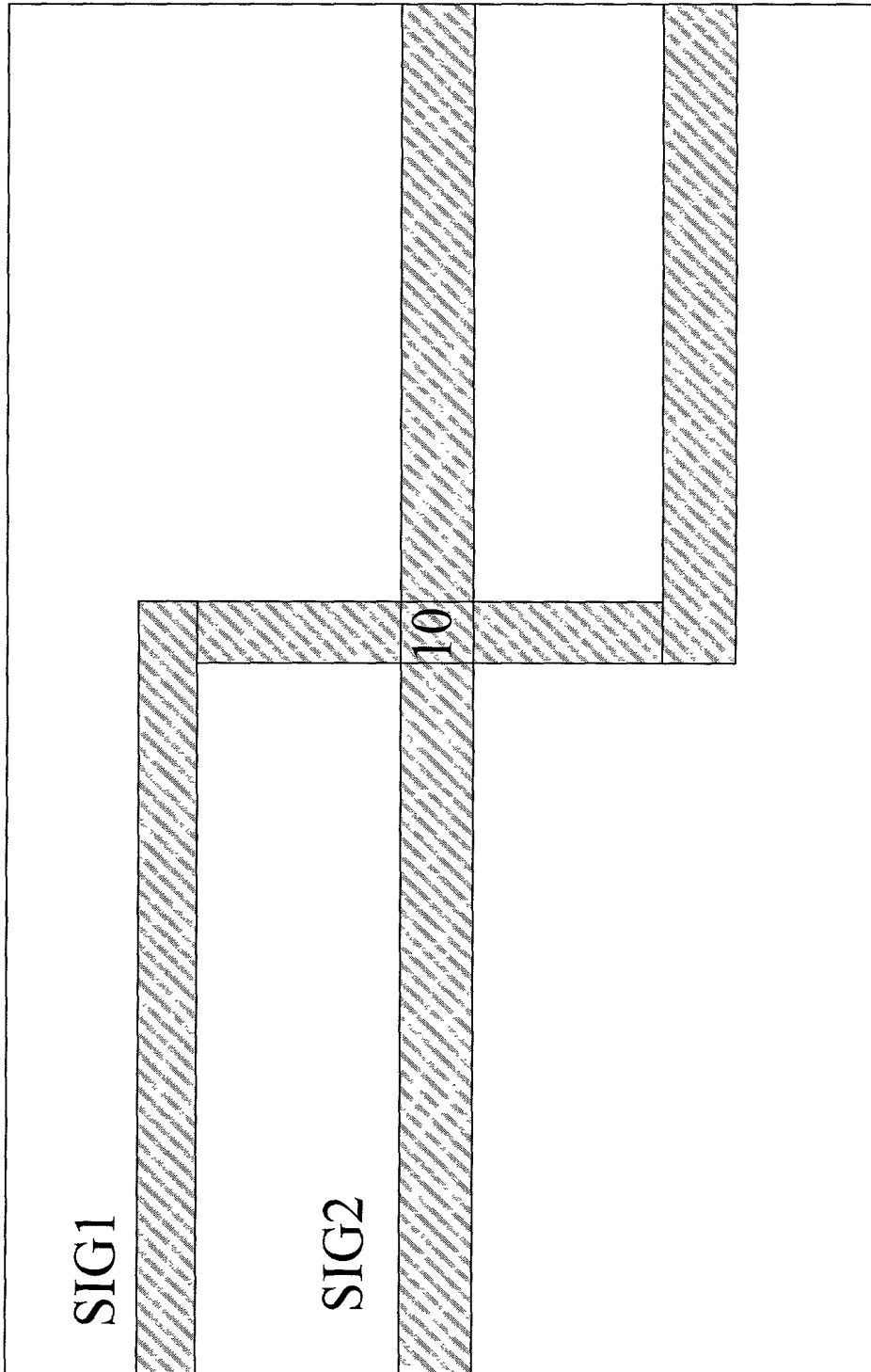


Fig. 1

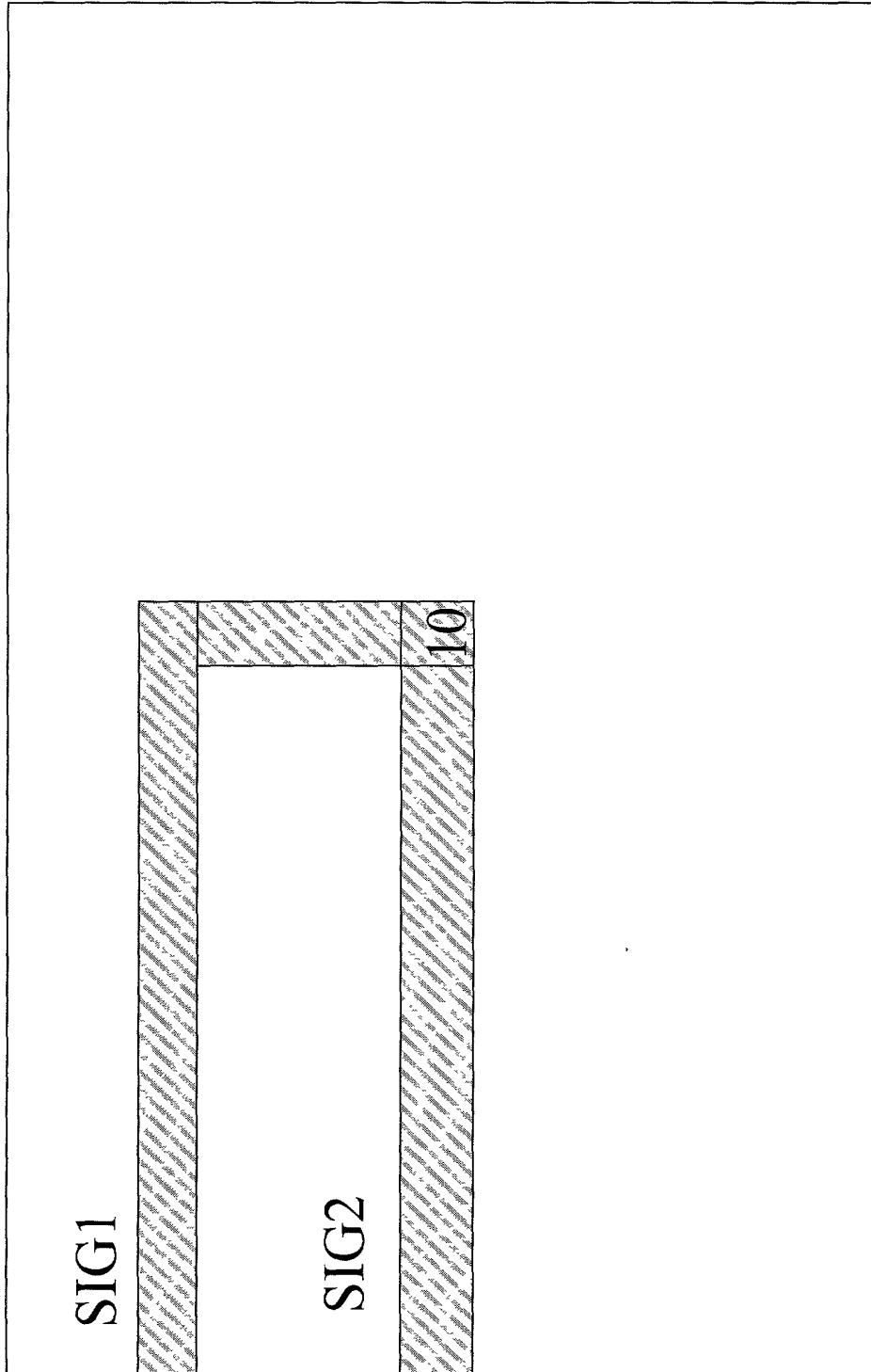


Fig. 2

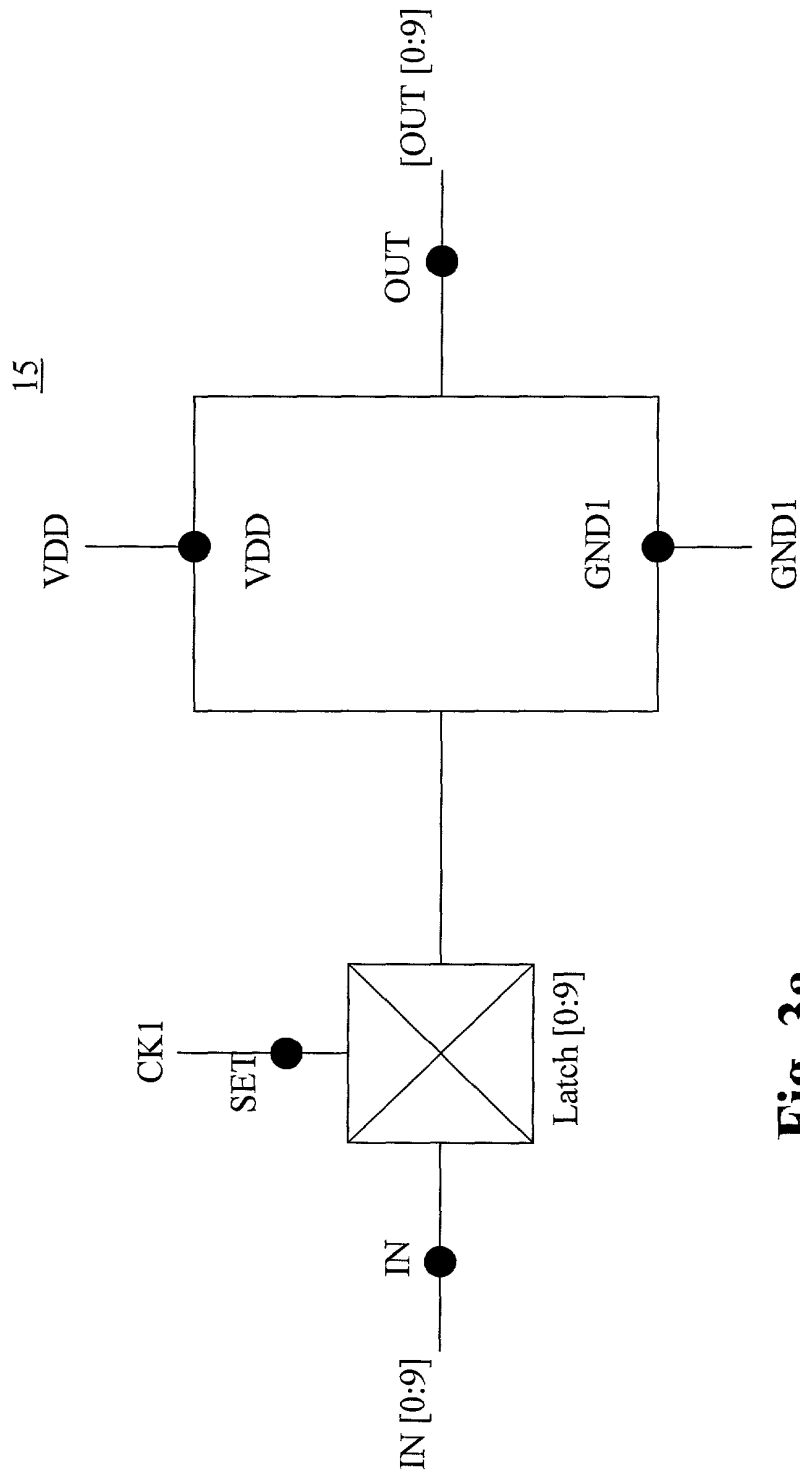


Fig. 3a

```
BLOCK parent 0;
  SIGNALS NET:
    CK1, GND1, IN[0:9], OUT[0:9], VDD;
  END SIGNALS
  INSTANCE LIST
    latch[0]: GND=GND1, IN=IN[0], OUT=OUT[0], SET=CK1, VDD=VDD;
    latch[1]: GND=GND1, IN=IN[1], OUT=OUT[1], SET=CK1, VDD=VDD;
    latch[2]: GND=GND1, IN=IN[2], OUT=OUT[2], SET=CK1, VDD=VDD;
    latch[3]: GND=GND1, IN=IN[3], OUT=OUT[3], SET=CK1, VDD=VDD;
    latch[4]: GND=GND1, IN=IN[4], OUT=OUT[4], SET=CK1, VDD=VDD;
    latch[5]: GND=GND1, IN=IN[5], OUT=OUT[5], SET=CK1, VDD=VDD;
    latch[6]: GND=GND1, IN=IN[6], OUT=OUT[6], SET=CK1, VDD=VDD;
    latch[7]: GND=GND1, IN=IN[7], OUT=OUT[7], SET=CK1, VDD=VDD;
    latch[8]: GND=GND1, IN=IN[8], OUT=OUT[8], SET=CK1, VDD=VDD;
    latch[9]: GND=GND1, IN=IN[9], OUT=OUT[9], SET=CK1, VDD=VDD;
  END INSTANCE LIST
ENDBLOCK
```

Fig. 3b

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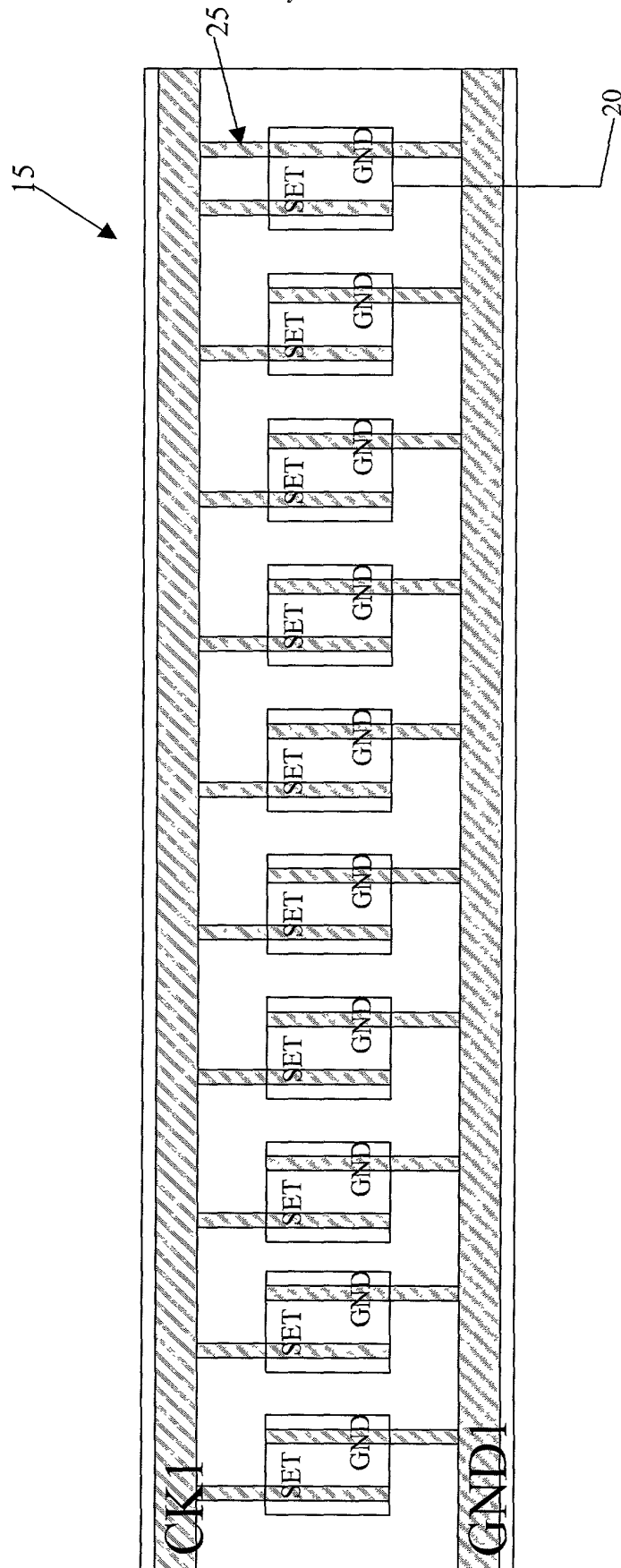


Fig. 3c

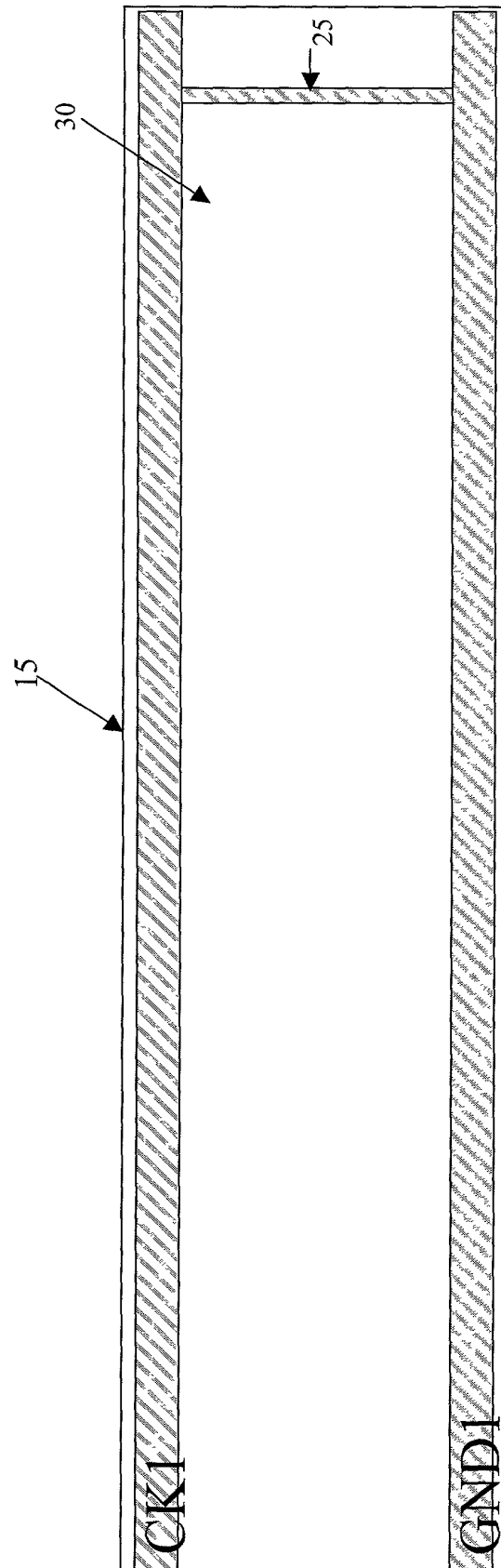


Fig. 4

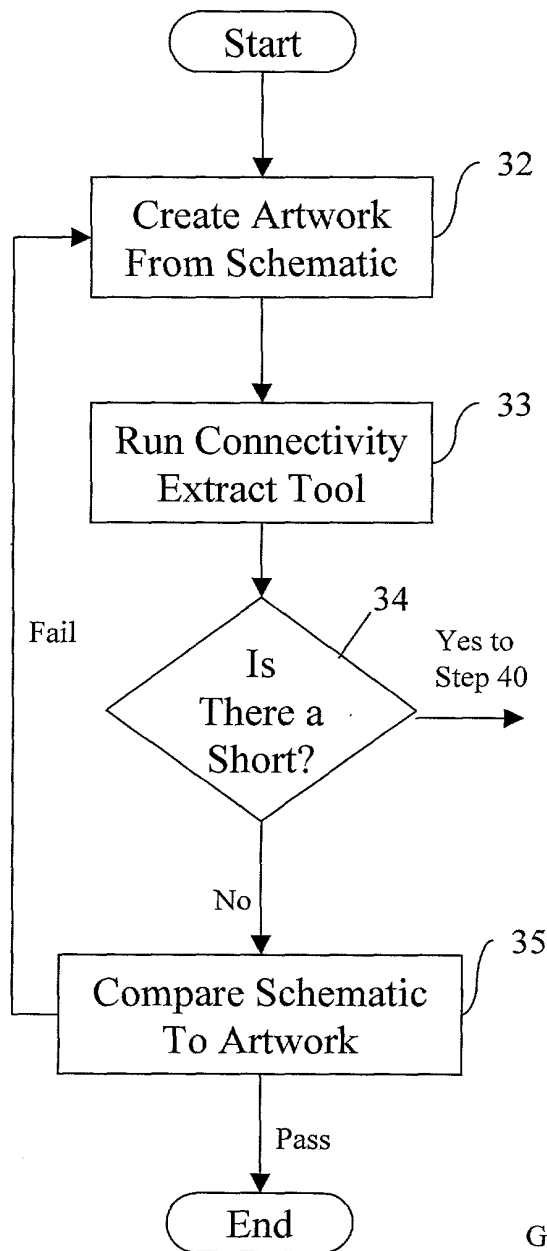


Fig. 5a

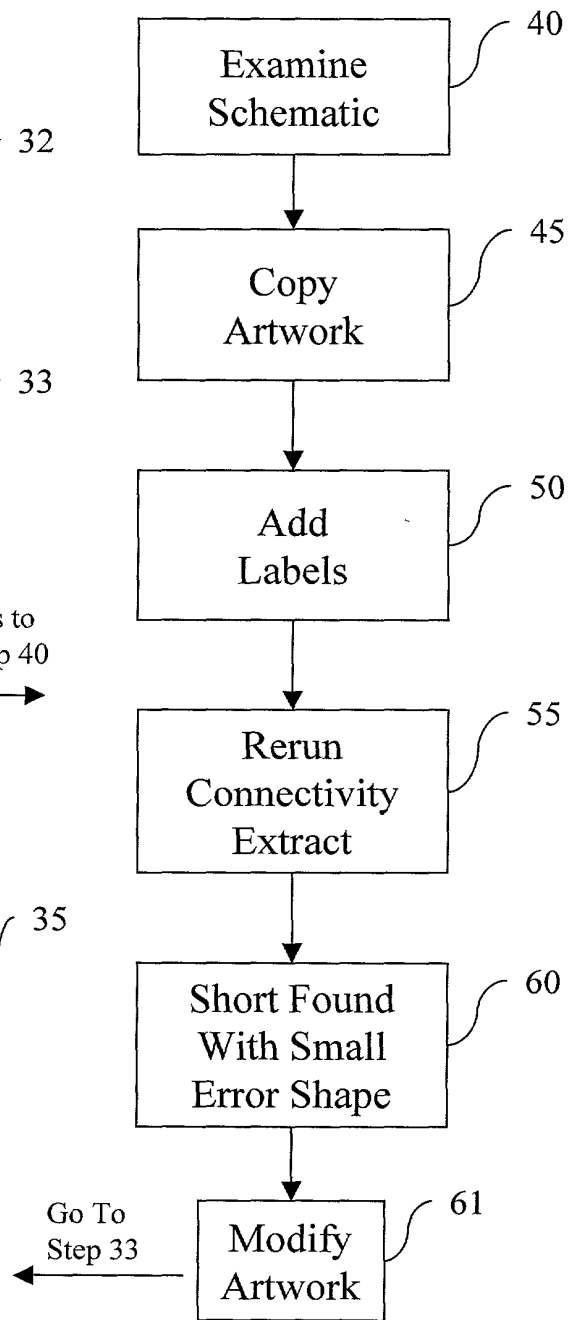


Fig. 5b

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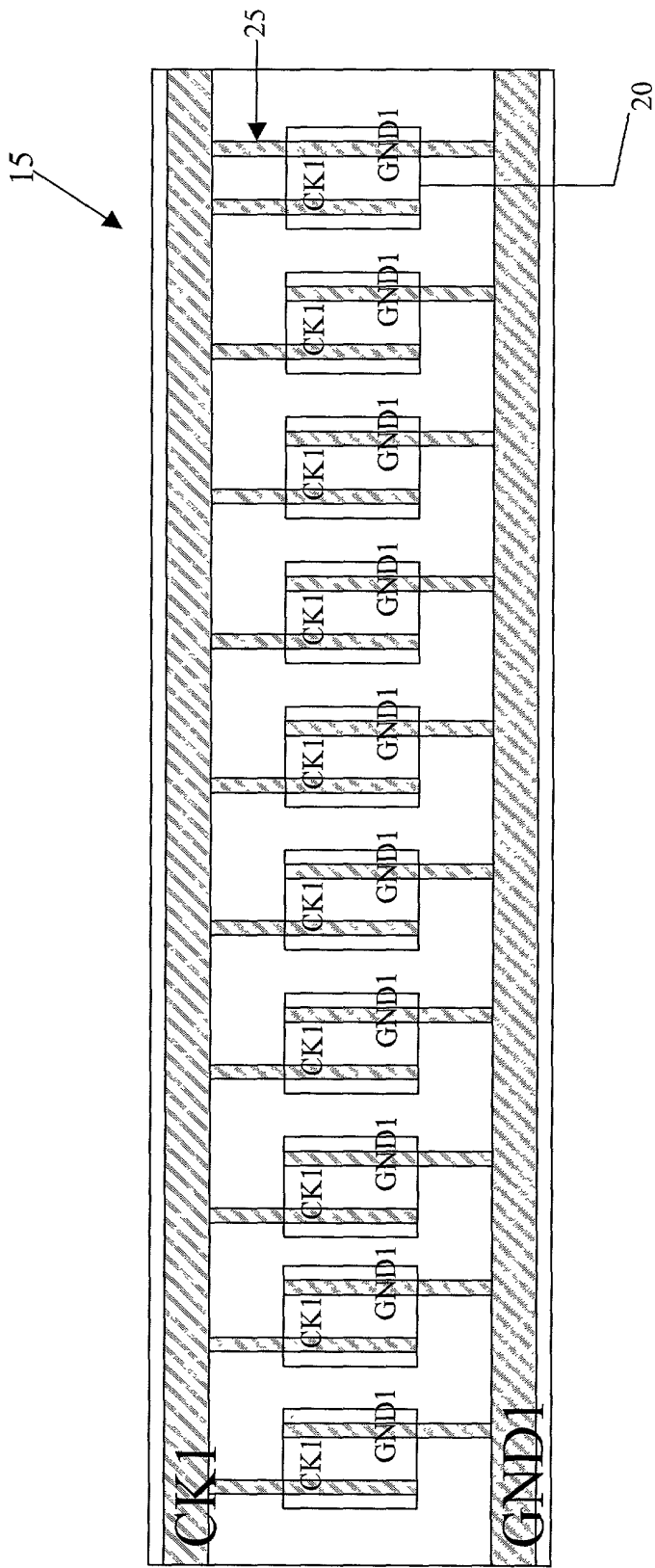


Fig. 6

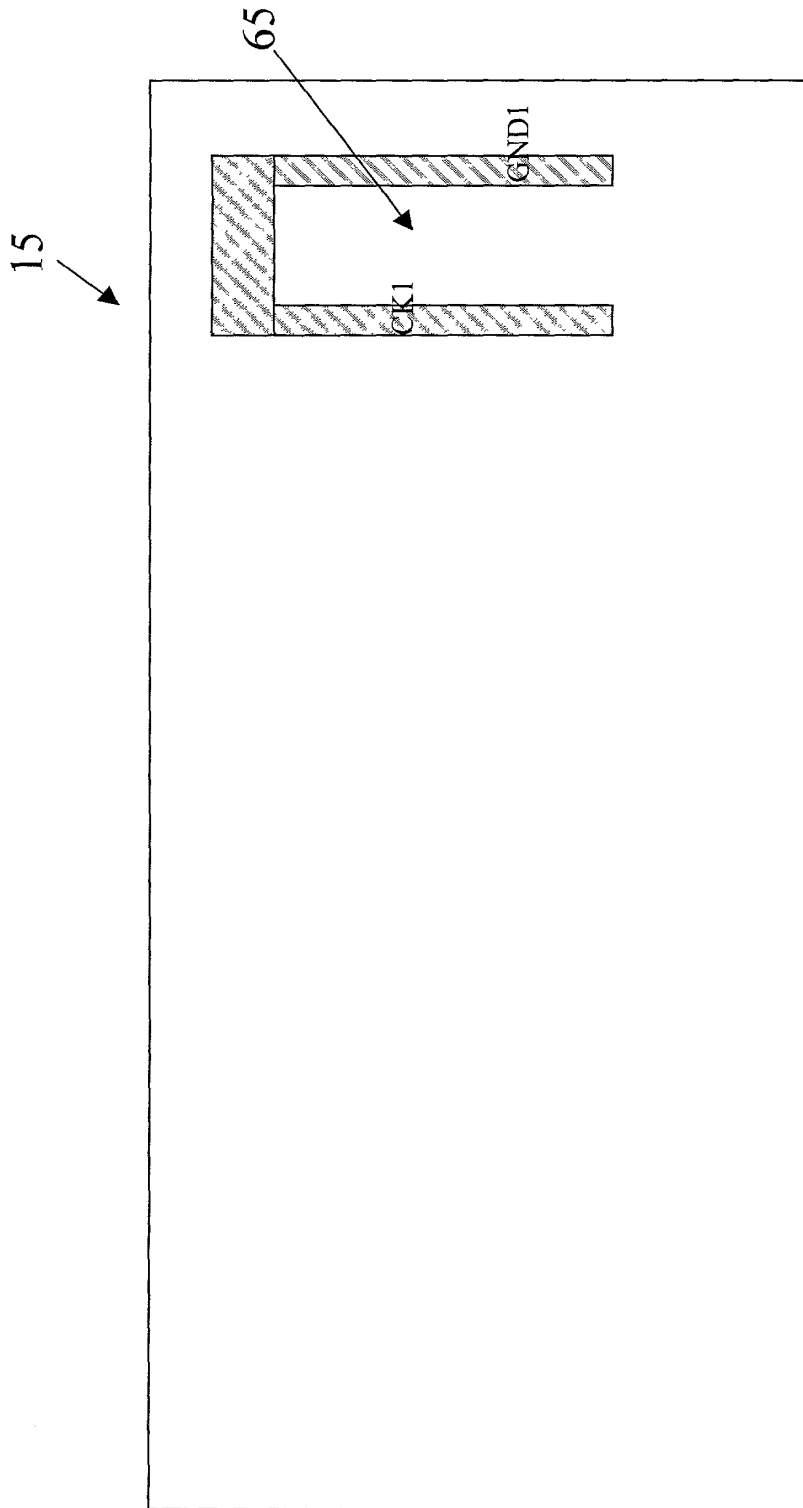


Fig. 7